Application Serial No. 09/708,339, Attorney Docket No. SILA:035C1, titled "Method and Apparatus for Operating a PLL with a Phase Detector/Sample Hold Circuit for Synthesizing High-Frequency Signals for Wireless Communications," filed on November 8, 2000, which is a continuation of U.S. Patent Application Serial No. 09/087,017, filed on May 29, 1998, now U.S. Patent 6,167,245.

Please replace the paragraph beginning on page 1, line 26 and ending on page 2, line 7 with the following:

Furthermore, this patent application claims priority to: Provisional U.S. Patent Application Serial No. 60/261,506, Attorney Docket No. SILA:072PZ1, filed on January 12, 2001; Provisional U.S. Patent Application Serial No. 60/273,119, Attorney Docket No. SILA:072PZ2, titled "Partitioned RF Apparatus with Digital Interface and Associated Methods," filed on March 2, 2001. This patent application also claims priority to, and incorporates by reference: Provisional U.S. Patent Application Serial No. 60/333,940, Attorney Docket No. SILA:074PZ1, titled "Apparatus and Methods for Generating Radio Frequencies in Communication Circuitry," filed on November 28, 2001; Provisional U.S. Patent Application Serial No. 60/339,819, Attorney Docket No. SILA:074PZ2, titled "Radio-Frequency Communication Apparatus and Associated Methods," filed on December 13, 2001; U.S. Patent Application Serial No. 10/075,122, Attorney Docket No. SILA:078, titled "Digital Architecture for Radio-Frequency Apparatus and Associated Methods"; U.S. Patent Application Serial No. 10/075,199, Attorney Docket No. SILA:097, titled "Notch Filter for DC Offset Reduction in Radio-Frequency Apparatus and Associated Methods"; and U.S. Patent Application Serial No. 10/074,676, Attorney Docket No. SILA:098, titled "DC Offset Reduction in Radio-Frequency Apparatus and Associated Methods."